

<b>L Number</b>	<b>Hits</b>	<b>S arch T xt</b>	<b>DB</b>	<b>Tim stamp</b>
<b>1</b>	<b>100</b>	("5247455" "5321885" "5628852" "5686702" "4587727" "4763407" "5268813" "5534666" "5590461" "4314115" "4536051" "4801876" "5512712" "5982468" "6085018" "5262954" "6021025" "4510446" "4549206" "4782283" "5325582" "5558522" "5621913" "5784484" "5909452" "6147870" "6185273" "5008619" "5821614" "4627005" "5530622" "5718038" "5743990" "6144557" "4285123" "4404059" "4628598" "4759809" "4790895" "4806957" "4863550" "5206463" "5671531" "5685071" "5710693" "5788855" "5834705" "5940964" "6053395" "6061260").pn.	<b>USPAT;            US-P PUB;            EP ; JP ;            DERWENT;            IBM_TDB</b>	<b>2002/09/04            13:59</b>

2	137246	"print d ircuit b ard"	USPAT; EP ; JP ; DERWENT; US CR	2002/09/04 14:47
3	605	"print d cir uit b ard" and sch duling	USPAT; EP ; JP ; DERWENT; USOCR	2002/09/04 14:47
4	179	("printed circuit board" and scheduling) and layout	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 14:47
6	10	((("printed circuit board" and scheduling) and layout) and assembly) and CAD	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 14:48
5	99	("printed circuit board" and scheduling) and layout) and assembly	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 14:59
7	0	("printed circuit board" and scheduling) and "board arrangment"	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:00

8	0	("5247455" "5321885" "5628852" "5686702" "4587727" "4763407" "5268813" "5534666" "5590461" "4314115" "4536051" "4801876" "5512712" "5982468" "6085018" "5262954" "6021025" "4510446" "4549206" "4782283" "5325582" "5558522" "5621913" "5784484" "5909452" "6147870" "6185273" "5008619" "5821614" "4627005" "5530622" "5718038" "5743990" "6144557" "4285123" "4404059" "4628598" "4759809" "4790895" "4806957" "4863550" "5206463" "5671531" "5685071" "5710693" "5788855" "5834705" "5940964" "6053395" "6061260").pn. ) and "b ard arrangm nt"	USPAT; EP ; JP ; DERWENT; US CR	2002/09/04 15:00
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9	351	("print d circuit b ard" and sch duling) and arrang m nt	USPAT; EP ; JP ; DERWENT; US CR	2002/09/04 15:00
10	0	((("print d ircuit b ard" and scheduling) and arrang m nt) and (pcb near arrang m nt)	USPAT; EP ; JPO; DERWENT; USOCR	2002/09/04 15:01
11	6	((("printed circuit board" and scheduling) and arrangement) and (board near arrangement)	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:02
12	31395	700/\$.ccls.	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:03
13	48	("printed circuit board" and scheduling) and 700/\$.ccls.	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:12
14	222473	29/\$.ccls.	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:13

15	17	29/\$.ccls. and ("5247455" "5321885" "5628852" "5686702" "4587727" "4763407" "5268813" "5534666" "5590461" "4314115" "4536051" "4801876" "5512712" "5982468" "6085018" "5262954" "6021025" "4510446" "4549206" "4782283" "5325582" "5558522" "5621913" "5784484" "5909452" "6147870" "6185273" "5008619" "5821614" "4627005" "5530622" "5718038" "5743990" "6144557" "4285123" "4404059" "4628598" "4759809" "4790895" "4806957" "4863550" "5206463" "5671531" "5685071" "5710693" "5788855" "5834705" "5940964" "6053395" "6061260").pn. )	USPAT; EPO; JPO; DERWENT; US CR	2002/09/04 15:15
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16	0	"G rb r adj Garm nt"	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:15
17	541	rb r adj Garm nt	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:15
19	0	((Gerber adj Garment) and waste) and "waste minimization"	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:16
18	55	(Gerber adj Garment) and waste	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:21
20	185	cutting near "circuit boards"	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:22
21	8	(cutting near "circuit boards") and waste	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:26
22	0	(cutting near "circuit boards") and scheduling	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:26
23	0	(cutting near "circuit boards") and schedul	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:26
24	34	(cutting near "circuit boards") and production	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:30
25	837	PCB near manufacturing	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:30
26	390	(PCB near manufacturing) and (printed near circuit)	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:31
27	70	((PCB near manufacturing) and (printed near circuit)) and cut	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:37
28	204	"printed wiring board ass mbli s"	USPAT; EP ; JP ; DERWENT; USOCR	2002/09/04 15:39

29	63	"print d wiring b ard ass mbli s" and manufacturing	USPAT; EP ; JPO; DERWENT; US CR	2002/09/04 15:39
30	4	("printed wiring b ard assembli s" and manufacturing) and "manufacturing system"	USPAT; EP ; JPO; DERWENT; USOCR	2002/09/04 15:43
31	10	"5321885"	USPAT; EPO; JPO; DERWENT; USOCR	2002/09/04 15:44
32	14	5516028.URPN.	USPAT	2002/09/04 15:51
33	23	("3421211"   "3803556"   "4256296"   "4504919"   "4564102"   "4604704"   "4694570"   "4706187"   "4719694"   "4731567"   "4759488"   "4821197"   "4850104"   "4980970"   "5067648"   "5086557"   "5170554"   "5268849"   "5321885"   "5335601"   "5355579"   "5536128"   "5680696").PN.	USPAT	2002/09/04 15:58